DUAL-BAND LOW NOISE AMPLIFIER/MIXER

Typical Applications

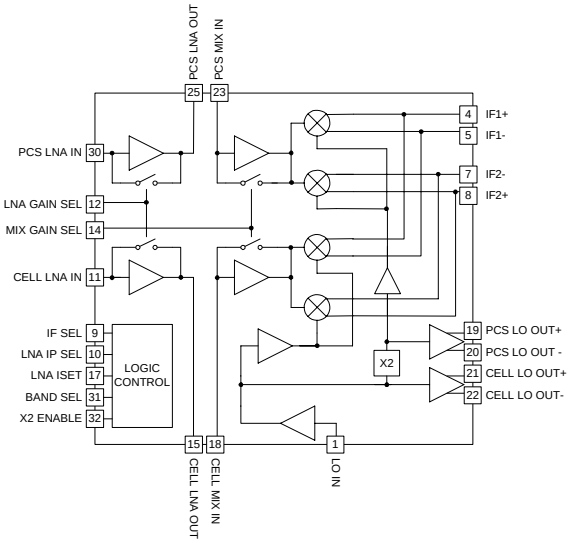
- TDMA-GSM Cellular/PCS Handsets
- TDMA Cellular/PCS Handsets
- GAIT Handsets
- CDMA Cellular/PCS Handsets
- GSM DCS/PCS Handsets

Product Description

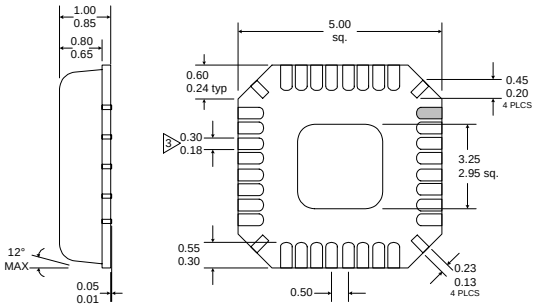
The RF2492 is a fully featured dual-band LNA/Mixer and is usable in a variety of mobile handset applications. The unique dual IF outputs provide interface to two independent IF SAW filters supporting applications such as TDMA-EDGE where 30kHz and 200kHz bandwidth SAW filters are used. With independent power management control pins for the LNAs and mixers, either IF output can be accessed from either high- or low-band LNAs, providing maximum flexibility with minimum power usage. Multiple gain control options are provided to achieve a very large dynamic range for the receiver. A frequency doubler is included in the LO circuit to generate both high- and low-band LO signals with a single VCO. The RF2492 is packaged in a 32 pin, 5mmx5mm, leadless plastic package.

Optimum Technology Matching® Applied

- ☐ Si BJT
- ☐ Si Bi-CMOS
- ☐ GaAs HBT
- ☑ SiGe HBT
- ☐ GaAs MESFET
- ☐ Si CMOS



Functional Block Diagram



NOTES:

1. Shaded Pin is Lead 1.
2. Pin 1 identifier must exist on top surface of package by identification mark or feature on the package body. Exact shape and size is optional.
3. Dimension applies to plated terminal: to be measured between 0.02 mm and 0.25 mm from terminal end.
4. Package Warpage: 0.05 mm max.
5. Die Thickness Allowable: 0.305 mm max.

Package Style: LCC, 32-Pin, 5x5

Features

- Complete Dual-Band Receiver Front End
- Low Noise Figure
- Stepped LNA/Mixer Gain Control
- Adjustable LNA Bias Current/IIP3
- Integrated LO Frequency Doubler
- Differential LO Buffer Outputs

Ordering Information

RF2492	Dual-Band Low Noise Amplifier/Mixer
RF2492 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc.
7625 Thorndike Road
Greensboro, NC 27409, USA

Tel (336) 664 1233
Fax (336) 664 0454
http://www.rfmd.com

Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +5.0	V_{DC}
Input LO and RF Levels	+6	dBm
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C

Caution! ESD sensitive device.

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall					T = 25°C, V_{CC} = 2.75V
RF Frequency Range		881		MHz	Specifications
		1960		MHz	Specifications
	800		950	MHz	Usable range
	1700		2100	MHz	Usable range
IF Frequency Range		110		MHz	Specifications
	50		250	MHz	Usable range
Cell LO Buffer Output	-13.0	-12.7		dBm	T1 insertion loss not considered.
PCS LO Buffer Output	-14.0	-12.7		dBm	T2 insertion loss not considered.
Cellular Band					Freq=869MHz to 894MHz
LNA (On)					RF=880MHz and 881MHz
Gain	13.5	14.5	15.5	dB	LNA set for max IIP3
	13.5	14	15	dB	LNA set for Nominal IIP3
Noise Figure		2.0	2.2	dB	LNA set for max IIP3
		1.8	2.0	dB	LNA set for Nominal IIP3
Input IP3	+7.0	+7.5		dBm	LNA set for max IIP3
	+1.5	+3.0	+7.0	dBm	LNA set for Nominal IIP3
LNA (Off)					
Gain	-8.5	-7.5	-6.5	dB	
Noise Figure	5.5	5.8	6.0	dB	
Input IP3	+20.0	+21.0		dBm	
Mixer					IF Select=1, (IF1) RF=880MHz, LO=990MHz@-5dBm
Gain	12	13	14	dB	Mixer RF amp ON; Z_{LOAD}=1kΩ single-ended
	3.5	4.4	5.0	dB	Mixer RF amp OFF
Noise Figure	6.5	7.0	7.5	dB	Mixer RF amp ON
	13.5	14.0	14.5	dB	Mixer RF amp OFF
Input IP3	+2.0	+4.5		dBm	Mixer RF amp ON
	+11.5	+13.8		dBm	Mixer RF amp OFF
LO Input Level		-5		dBm	Specifications
	-10		+3	dBm	Usable range
Mixer					IF Select=0, (IF2) RF=880MHz, LO=990MHz@-5dBm
Gain	12	13	14	dB	Mixer RF amp ON; Z_{LOAD}=1kΩ single-ended
	3.0	4.4		dB	Mixer RF amp OFF
Noise Figure	6.8	7.2	7.4	dB	Mixer RF amp ON
	13.7	14.0	14.2	dB	Mixer RF amp OFF
Input IP3	+2.0	+4.5		dBm	Mixer RF amp ON
	+11.5	+14.0		dBm	Mixer RF amp OFF
LO Input Level		-5		dBm	Specifications
	-10		+3	dBm	Usable range

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Mixer Isolation					
LO to RF	40	42		dB	Mixer RF amp ON
LO to RF	33.5	35		dB	Mixer RF amp OFF
RF to LO	56.5	63		dB	Mixer RF amp ON
RF to LO	54.5	60.5		dB	Mixer RF amp OFF
LO to IF1	24	36.5		dB	Mixer RF amp ON
LO to IF2	28	42		dB	Mixer RF amp ON
IF1 to RF	50	51		dB	Mixer RF amp ON
RF to IF1	43.5	44.5		dB	Mixer RF amp ON
IF2 to RF	49.5	50.5		dB	Mixer RF amp ON
RF to IF2	43	45		dB	Mixer RF amp ON
Cascade (LNA On)					IF Select=1, 3dB RF Filter Insertion Loss
Gain		24.5		dB	LNA set for max IIP3; Mixer RF amp ON
		24		dB	LNA set for Nominal IIP3; Mixer RF amp ON
		15.9		dB	LNA set for max IIP3; Mixer RF amp OFF
		15.4		dB	LNA set for Nominal IIP3; Mixer RF amp OFF
Noise Figure		2.8		dB	LNA set for max IIP3; Mixer RF amp ON
		2.7		dB	LNA set for Nominal IIP3; Mixer RF amp ON
		5.3		dB	LNA set for max IIP3; Mixer RF amp OFF
		5.4		dB	LNA set for Nominal IIP3; Mixer RF amp OFF
Input IP3		-6.5		dBm	LNA set for max IIP3; Mixer RF amp ON
		-6.4		dBm	LNA set for Nominal IIP3; Mixer RF amp ON
		+1.1		dBm	LNA set for max IIP3; Mixer RF amp OFF
		0		dBm	LNA set for Nominal IIP3; Mixer RF amp OFF
Cascade (LNA Off)					IF Select=1, 3dB RF Filter Insertion Loss
Gain		2.5		dB	Mixer RF amp ON
		-6.0		dB	Mixer RF amp OFF
Noise Figure		17.4		dB	Mixer RF amp ON
		24.5		dB	Mixer RF amp OFF
Input IP3		+14.5		dBm	Mixer RF amp ON
		+19.0		dBm	Mixer RF amp OFF
PCS Band					Freq=1930MHz to 1990MHz
LNA (On)					RF=1960MHz and 1961MHz
Gain	13.0	14.8		dB	LNA set for max IIP3
	13.0	14.0		dB	LNA set for Nominal IIP3
Noise Figure		1.5	1.9	dB	LNA set for max IIP3
	1.7	1.8	1.9	dB	LNA set for Nominal IIP3
Input IP3	+7.0	+9.0		dBm	LNA set for max IIP3
	+0.5	+3.5		dBm	LNA set for Nominal IIP3
LNA (Off)					
Gain	-7.0	-5.5		dB	
Noise Figure	6.0	6.2	6.5	dB	
Input IP3	+20.0	+25.0		dBm	
Mixer					RF=1960MHz, LO=1035MHz@-5dBm, IF Select=1 (IF1)
Gain	11.5	12.5		dB	Mixer RF amp ON; Z_{LOAD}=1kΩ single-ended
	2.0	3.5		dB	Mixer RF amp OFF
Noise Figure	8.0	8.5	9.0	dB	Mixer RF amp ON
	15.0	15.5	16.0	dB	Mixer RF amp OFF
Input IP3	+4.0	+6.0		dBm	Mixer RF amp ON
	+14.0	+16.0		dBm	Mixer RF amp OFF
LO Input Level		-5		dBm	Specifications
	-10		+3	dBm	Usable range

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Mixer					RF=1960MHz, LO=1035MHz@-5dBm, IF Select=0 (IF2)
Gain	11.0	12.6		dB	Mixer RF amp ON; Z_{LOAD}=1kΩ single-ended
	2.0	3.5		dB	Mixer RF amp OFF
Noise Figure	8.0	8.4	9.0	dB	Mixer RF amp ON
	15.0	15.5	16.5	dB	Mixer RF amp OFF
Input IP3	+3.5	+6.0		dBm	Mixer RF amp ON
	+14.0	+16.0		dBm	Mixer RF amp OFF
LO Input Level		-5		dBm	Specifications
	-10		+3	dBm	Usable range
Mixer Isolation					
LO to RF	55	57.5		dB	Mixer RF amp ON
LO to RF	54	63		dB	Mixer RF amp OFF
RF to LO	49.8	52.5		dB	Mixer RF amp ON
RF to LO	52	55.5		dB	Mixer RF amp OFF
LO to IF1	54.5	55.5		dB	Mixer RF amp ON
LO to IF2	58.5	59		dB	Mixer RF amp ON
IF1 to RF	49.5	51		dB	Mixer RF amp ON
RF to IF1	33	34.5		dB	Mixer RF amp ON
IF2 to RF	49	50.5		dB	Mixer RF amp ON
RF to IF2	41	42.5		dB	Mixer RF amp ON
Cascade (LNA On)					3dB RF Filter Insertion Loss
Gain		24.3		dB	LNA set for max IIP3; Mixer RF amp ON
		23.5		dB	LNA set for Nominal IIP3; Mixer RF amp ON
		15.3		dB	LNA set for max IIP3; Mixer RF amp OFF
		14.5		dB	LNA set for Nominal IIP3; Mixer RF amp OFF
Noise Figure		2.6		dB	LNA set for max IIP3; Mixer RF amp ON
		3.1		dB	LNA set for Nominal IIP3; Mixer RF amp ON
		5.7		dB	LNA set for max IIP3; Mixer RF amp OFF
		6.3		dB	LNA set for Nominal IIP3; Mixer RF amp OFF
Input IP3		-6		dBm	LNA set for max IIP3; Mixer RF amp ON
		-5.6		dBm	LNA set for Nominal IIP3; Mixer RF amp ON
		+3.0		dBm	LNA set for max IIP3; Mixer RF amp OFF
		+1.5		dBm	LNA set for Nominal IIP3; Mixer RF amp OFF
Cascade (LNA Off)					3dB RF Filter Insertion Loss
Gain		4		dB	Mixer RF amp ON
		-5		dB	Mixer RF amp OFF
Noise Figure		17		dB	Mixer RF amp ON
		24		dB	Mixer RF amp OFF
Input IP3		+14.0		dBm	Mixer RF amp ON
		+21.7		dBm	Mixer RF amp OFF
Power Supply					IF Select=1
Supply Voltage		2.75		V	Specifications
	2.7		3.6	V	Operating range
Supply Current		38	40	mA	Cellular; LNA On, Max IIP3, Mixer RF amp ON
		36	38	mA	Cellular; LNA On, Nom IIP3, Mixer RF amp ON
		32	34	mA	Cellular; LNA Off, Mixer RF amp ON
		26	28	mA	Cellular; LNA Off, Mixer RF amp OFF
LNA Current		40.0	42.5	mA	PCS; LNA On, Max IIP3, Mixer RF amp ON
		38.5	40.5	mA	PCS; LNA On, Nom IIP3, Mixer RF amp ON
		34.5	36.0	mA	PCS; LNA Off, Mixer RF amp ON
		28.0	29.5	mA	PCS; LNA Off, Mixer RF amp OFF

State Table (Typical Values for V_{CC}=2.75V and 3dB RF Filter Insertion Loss)

Parameter	Cellular						PCS					
	LNA On				LNA Off		LNA On				LNA Off	
	LNA at Max IIP3		LNA at Nom IIP3				LNA at Max IIP3		LNA at Nom IIP3			
	Mixer Amp On	Mixer Amp Off	Mixer Amp On	Mixer Amp Off	Mixer Amp On	Mixer Amp Off	Mixer Amp On	Mixer Amp Off	Mixer Amp On	Mixer Amp Off	Mixer Amp On	Mixer Amp Off
Cascade												
Gain (dB)	24.5	15.9	24	15.4	2	-6.8	24.3	15.3	23.5	14.5	4	-5
Noise Figure (dB)	2.8	5.3	2.7	5.4	18.2	25.2	2.6	5.7	3.1	6.3	17	24
Input IP3 (dBm)	-6.5	+1.1	-6.4	0	+14.9	+19.5	-6	+3	-5.6	+1.5	+14	+21.7
Total Current	38		36		32	26	40		38.5		34.5	28
LNA												
Gain (dB)	14.5	14.5	14	14	-7.5	-7.5	14.8	14.8	14	14	-5.5	-5.5
Noise Figure (dB)	2.0	2.0	1.8	1.8	5.8	5.8	1.5	1.5	1.8	1.8	6.2	6.2
Input IP3 (dBm)	+7.5	+7.5	+3	+3	+21	+21	+9	+9	+3.5	+3.5	25	25
Isolation (dB)												
LNA Current (mA)												
Mixer												
Gain (dB)	13	4.4	13	4.4	13	4.4	12.5	3.5	12.5	3.5	12.5	3.5
Noise Figure (dB)	7	14	7	14	7	14	8.5	15.5	8.5	15.5	8.5	15.5
Input IP3 (dBm)	+5.1	+13.8	+5.1	+13.8	+5.1	+13.8	+6	+16	+6	+16	+6	+16
LO Input Level (dBm)	-5	-5	-5	-5	-5	-5	-5	-5	-5	-5	-5	-5
LO to RF Isolation(dB)												
Mixer Current (mA)												

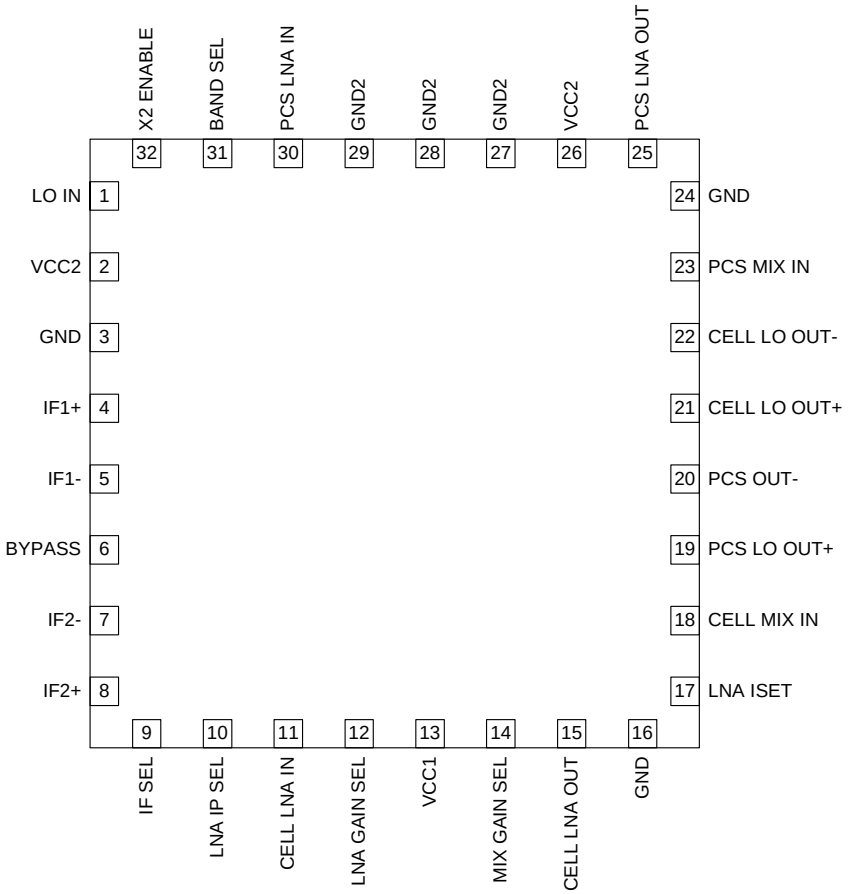
Control Logic Table

	BAND SEL	IF SEL	LNA GAIN	MIX GAIN	LNA IP SEL	ENABLE
PCS IF2 High Gain/High Linearity	1	0	1	1	1	1
PCS IF2 High Gain/Nominal Linearity	1	0	1	1	0	1
PCS IF2 Mid1 Gain	1	0	1	0	0	1
PCS IF2 Mid2 Gain	1	0	0	1	0	1
PCS IF2 Low Gain	1	0	0	0	0	1
Cell IF2 High Gain/ High Linearity	0	0	1	1	1	1
Cell IF2 High Gain/Nominal Linearity	0	0	1	1	0	1
Cell IF2 Mid1 Gain	0	0	1	0	0	1
Cell IF2 Mid2 Gain	0	0	0	1	0	1
Cell IF2 Low Gain	0	0	0	0	0	1
Cell IF1 High Gain/ High Linearity	0	1	1	1	1	1
Cell IF1 High Gain/ Nominal Linearity	0	1	1	1	0	1
Cell IF1 Mid1 Gain	0	1	1	0	0	1
Cell IF1 Mid2 Gain	0	1	0	1	0	1
Cell IF1 Low Gain	0	1	0	0	0	1
Shutdown	X	X	X	X	X	0
Not Defined	1	1	X	X	X	1

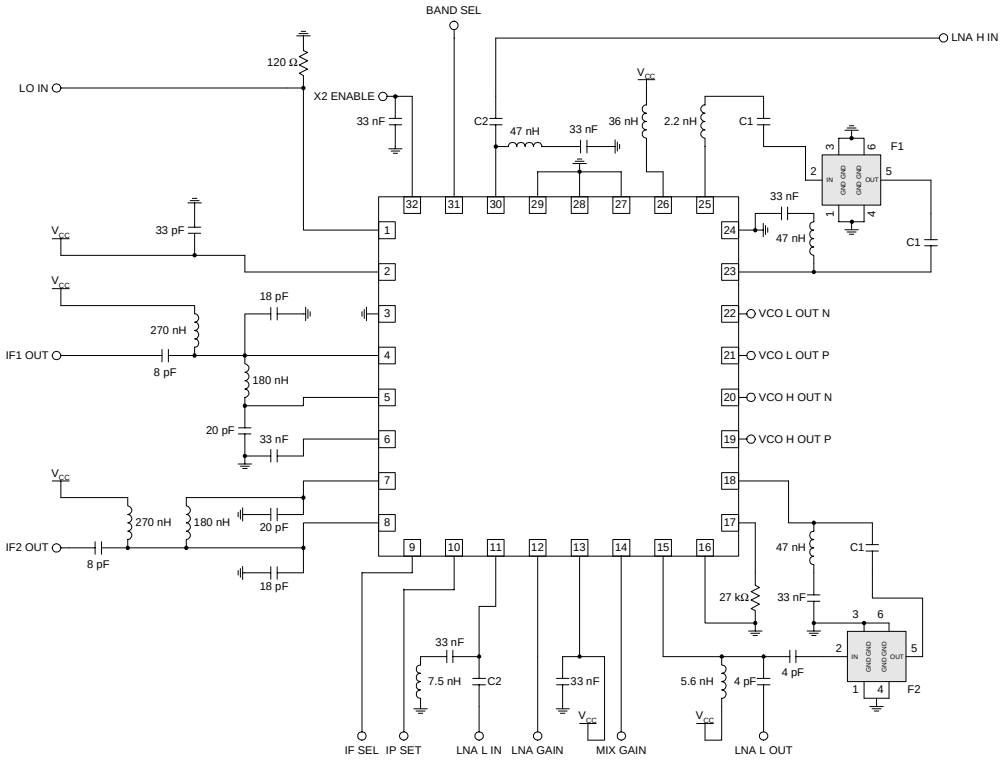
Pin	Function	Description	Interface Schematic
1	LO IN	LO input for both bands. Impedance is approximately (120-j25)Ω.	1kΩ shunt resistor (static bleed). AC coupled to transistor base (internal DC block).
2	VCC2	Power supply connection for internal LO amplifiers. External low-inductance bypass capacitor between 1nF and 47nF recommended.	
3	GND	Ground connection.	
4	IF1+	High-band IF output. Open collector. “Current combiner” IF interface to 1000Ω SAW filter recommended	2pF internal shunt capacitance.
5	IF1-	High-band IF output. Open collector. “Current combiner” IF interface to 1000Ω SAW filter recommended	2pF internal shunt capacitance.
6	BYPASS	Pin requires external bypass capacitor between 1nF and 47nF.	
7	IF2-	Low-band IF output. Open collector. “Current combiner” IF interface to 1000Ω SAW filter recommended	2pF internal shunt capacitance.
8	IF2+	Low-band IF output. Open collector. “Current combiner” IF interface to 1000Ω SAW filter recommended	2pF internal shunt capacitance.
9	IF SEL	Logic input. High selects IF1 mixer; low selects IF2 mixer.	Diode to V_{CC} and Ground. CMOS logic interface.
10	LNA IP SEL	Logic input. High selects external LNA current reference (pin 17); low selects internal LNA current reference.	Diode to V_{CC} and Ground. CMOS logic interface.
11	CELL LNA IN	Low-band LNA input (base). Simple external matching required for best performance.	
12	LNA GAIN SEL	Logic input. High selects maximum LNA gain; low selects minimum LNA gain.	Diode to V_{CC} and Ground. CMOS logic interface.
13	VCC1	Power supply for internal references, logic, and mixer preamplifiers. Internal RF bypass capacitor. External bypass capacitor between 1nF and 47nF required.	
14	MIX GAIN SEL	Logic input. High selects maximum mixer gain (mixer RF amp on); low selects minimum mixer gain (mixer RF amp off).	Diode to V_{CC} and Ground. CMOS logic interface.
15	CELL LNA OUT	Low-band LNA output (collector). Simple external L-C matching required.	
16	GND	Ground connection. See evaluation board layout.	
17	LNA ISET	External current reference for LNA. Resistor to ground sets LNA current when Pin 10 is high. 20kΩ results in approximately 10mA LNA current. Higher resistance results in lower current.	
18	CELL MIX IN	Low band mixer RF preamplifier input (base). External L-C network required for best performance.	
19	PCS LO OUT+	High-band buffered LO output.	Internal DC blocking capacitor. 1kΩ shunt resistor (static bleed).
20	PCS LO OUT-	High-band buffered LO output.	Internal DC blocking capacitor. 1kΩ shunt resistor (static bleed).
21	CELL LO OUT+	Low-band buffered LO output.	Internal DC blocking capacitor. 1kΩ shunt resistor (static bleed).
22	CELL LO OUT-	Low-band buffered LO output.	Internal DC blocking capacitor. 1kΩ shunt resistor (static bleed).
23	PCS MIX IN	High-band mixer RF preamplifier input (base). External L-C network required for best performance.	
24	GND	Ground connection. Keep traces physically short and connect immediately to ground plane (low-inductance ground required for best performance).	

Pin	Function	Description	Interface Schematic
25	PCS LNA OUT	High-band LNA output (collector). Small external inductance required for best impedance match to 50Ω.	
26	VCC2	High-band LNA power supply connection. Small series inductance required.	
27	GND2	High-band LNA emitter. Low-inductance ground required.	
28	GND2	Same as pin 27.	
29	GND2	Same as pin 27.	
30	PCS LNA IN	High-band LNA input (base). Simple external matching required for best performance.	
31	BAND SEL	Logic input. High selects high-band operation; low selects low-band operation.	Diode to V_{CC} and Ground. CMOS logic interface.
32	X2 ENABLE	Logic input. High enables LO doubler; low disables LO doubler.	Diode to V_{CC} and Ground. CMOS logic interface.
Die Flag	GND	Low inductance ground connection critical to proper operation.	

Pin Out


1 LO IN
2 VCC2
3 GND
4 IF1+
5 IF1-
6 BYPASS
7 IF2-
8 IF2+
9 IF SEL
10 LNA IP SEL
11 CELL LNA IN
12 LNA GAIN SEL
13 VCC1
14 MIX GAIN SEL
15 CELL LNA OUT
16 GND
17 LNA ISET
18 CELL MIX IN
19 PCS LO OUT+
20 PCS OUT-
21 CELL LO OUT+
22 CELL LO OUT-
23 PCS MIX IN
24 GND
25 PCS LNA OUT
26 VCC2
27 GND2
28 GND2
29 GND2
30 PCS LNA IN
31 BAND SEL
32 X2 ENABLE

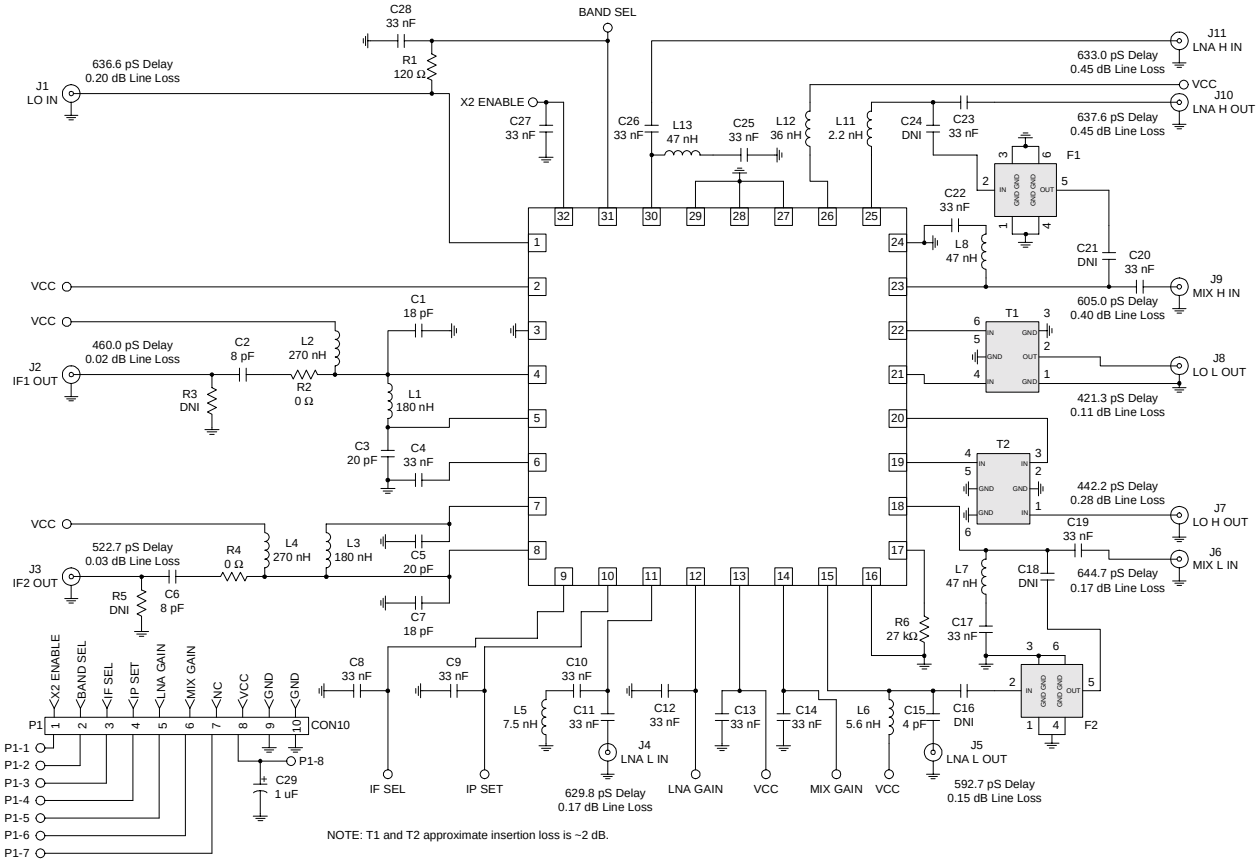
Application Schematic



NOTES:
1. C1's are not needed if filter DC impedance is open circuit (as is normally the case).
2. C2's are not needed if duplexer DC impedance is open circuit (as is normally the case).
3. IF interface shown is 50 Ω @ 110 MHz.

Evaluation Board Schematic
IF=110MHz

(Download Bill of Materials from www.rfmd.com.)

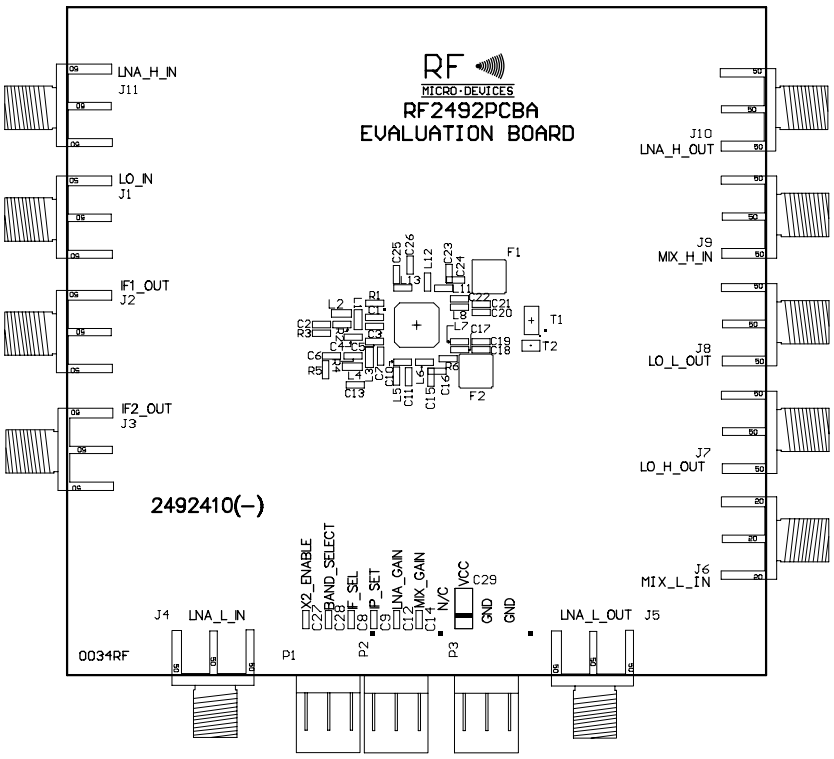


NOTE: T1 and T2 approximate insertion loss is ~2 dB.

Evaluation Board Layout
Board Size 3.0” x 3.0”

Board Thickness 0.0616”, Board Material FR-4, Multi-Layer

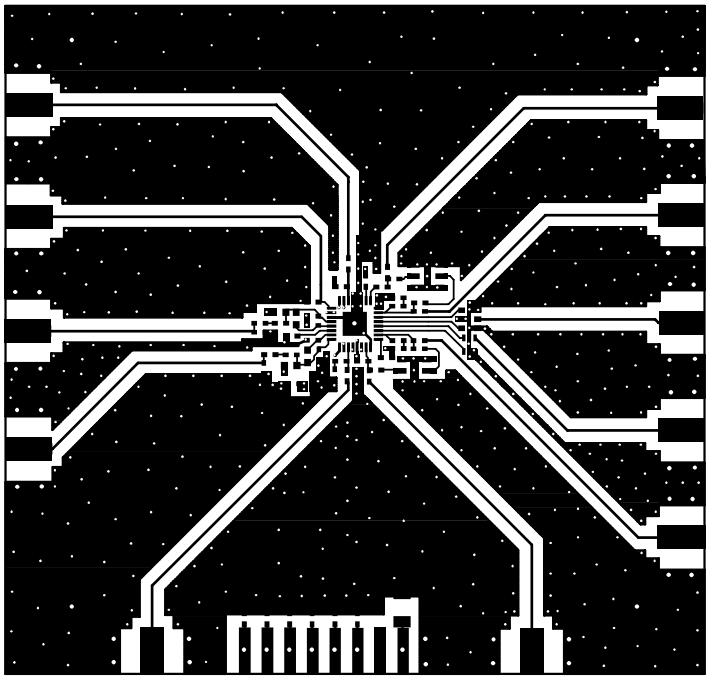
Assembly



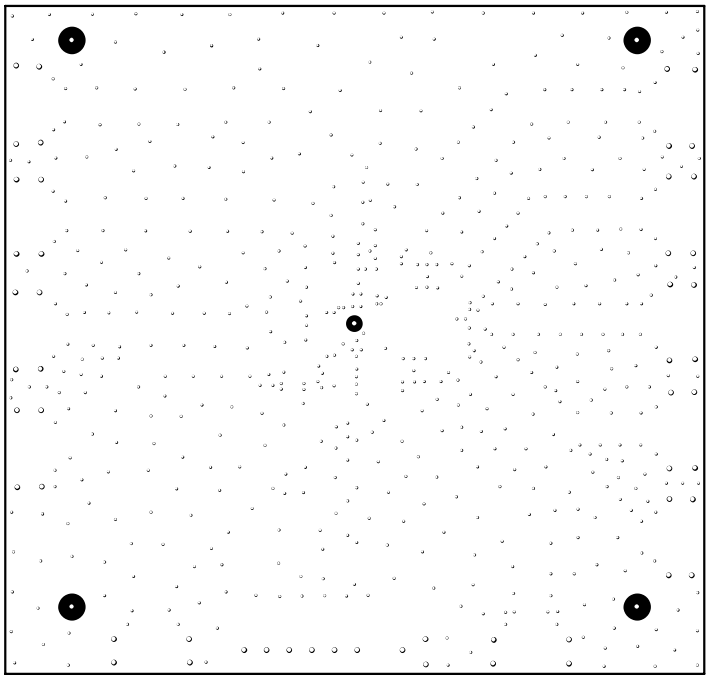
8

FRONT-ENDS

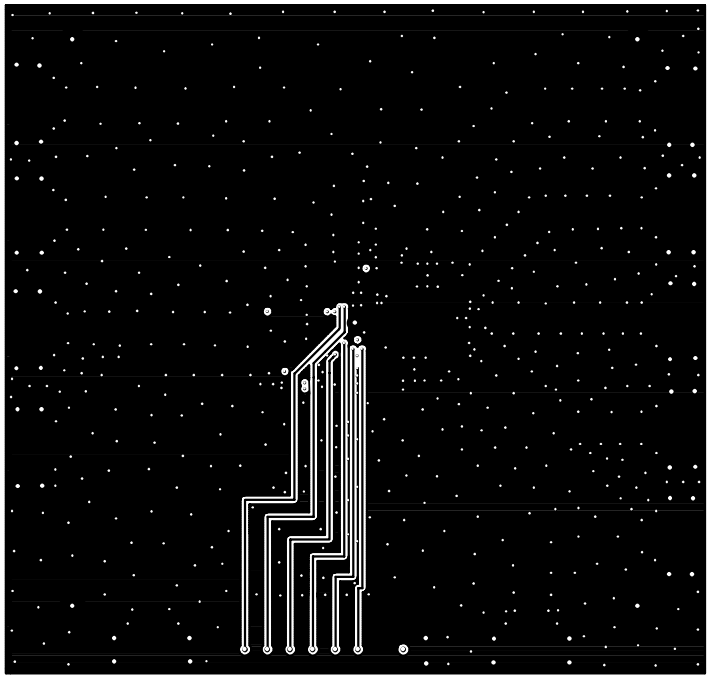
Top

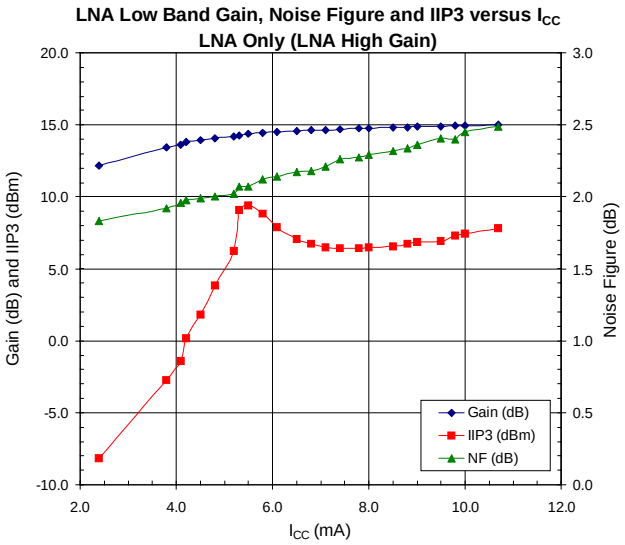


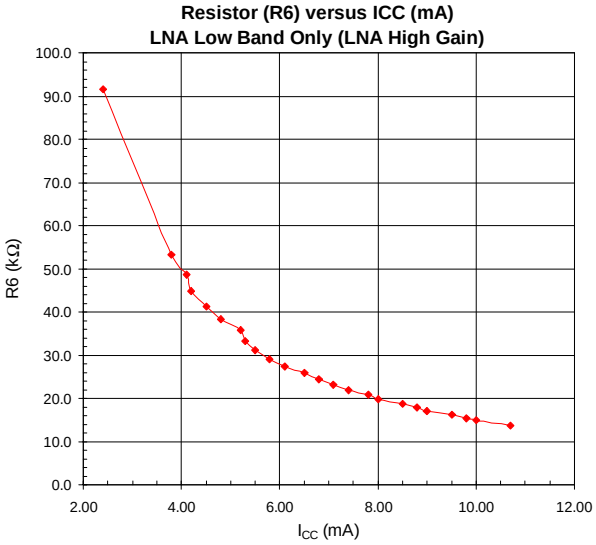
Typical Inner Layer

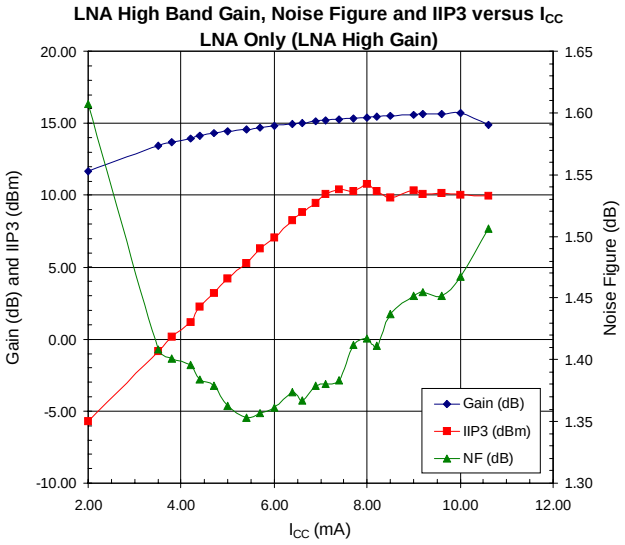


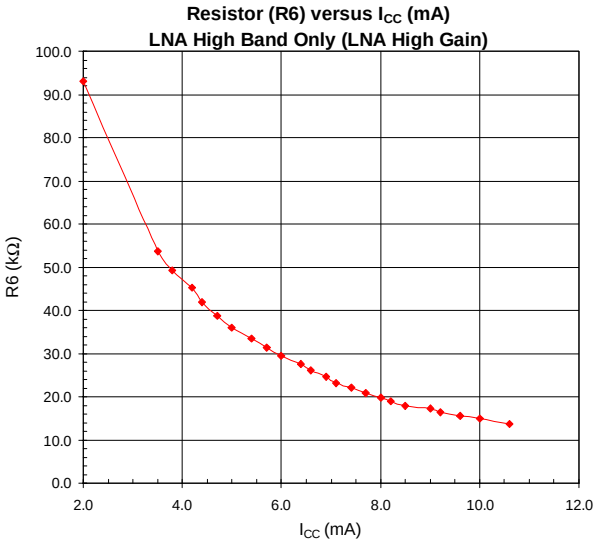
Back



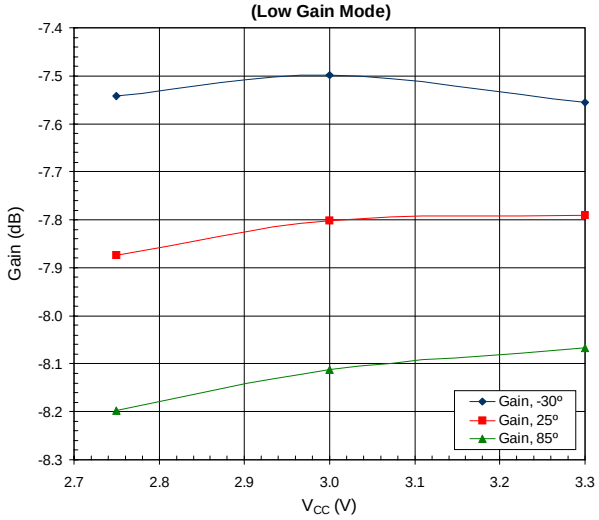

LNA Low Band Gain, Noise Figure and IIP3 versus ICC
LNA Only (LNA High Gain)
Gain (dB) and IIP3 (dBm)
Noise Figure (dB)
ICC (mA)
Gain (dB)
IIP3 (dBm)
NF (dB)


Resistor (R6) versus ICC (mA)
LNA Low Band Only (LNA High Gain)
R6 (kΩ)
ICC (mA)

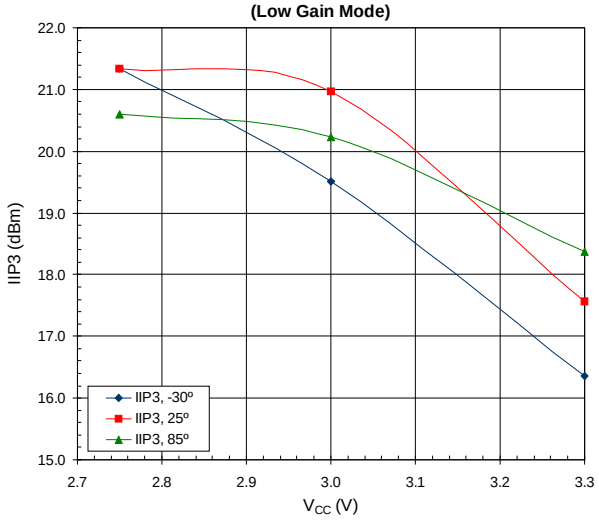

LNA High Band Gain, Noise Figure and IIP3 versus ICC
LNA Only (LNA High Gain)
Gain (dB) and IIP3 (dBm)
Noise Figure (dB)
ICC (mA)
Gain (dB)
IIP3 (dBm)
NF (dB)


Resistor (R6) versus ICC (mA)
LNA High Band Only (LNA High Gain)
R6 (kΩ)
ICC (mA)

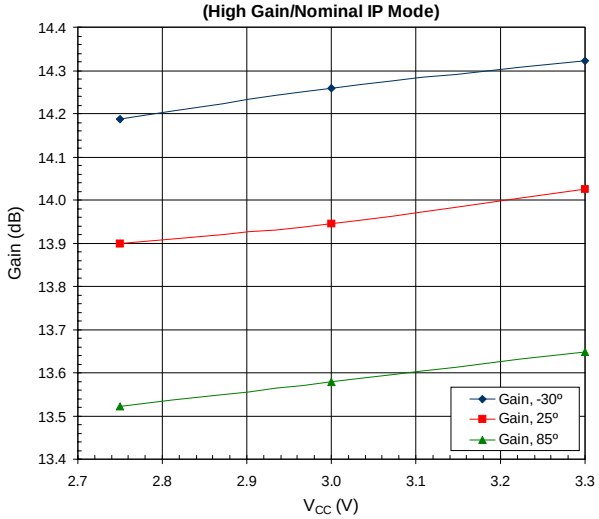
LNA Cellular Band
(Low Gain Mode)



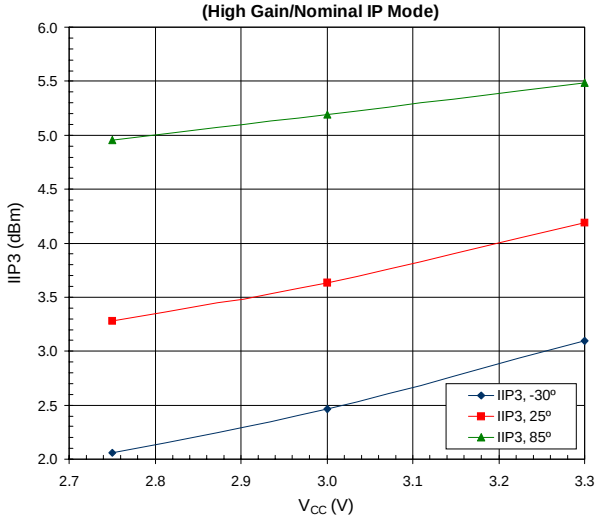
LNA Cellular Band
(Low Gain Mode)



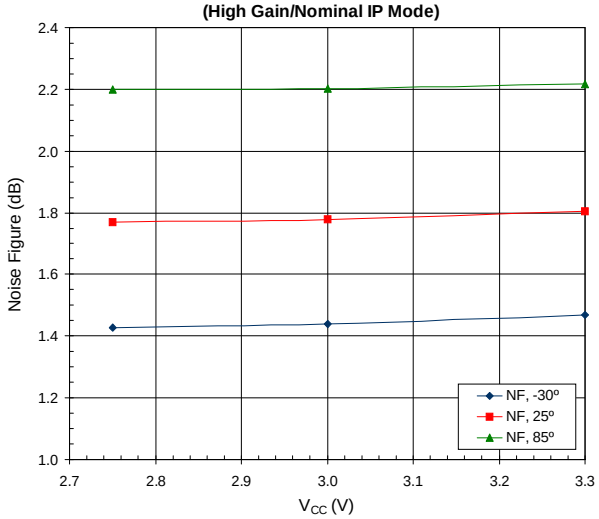
LNA Cellular Band
(High Gain/Nominal IP Mode)



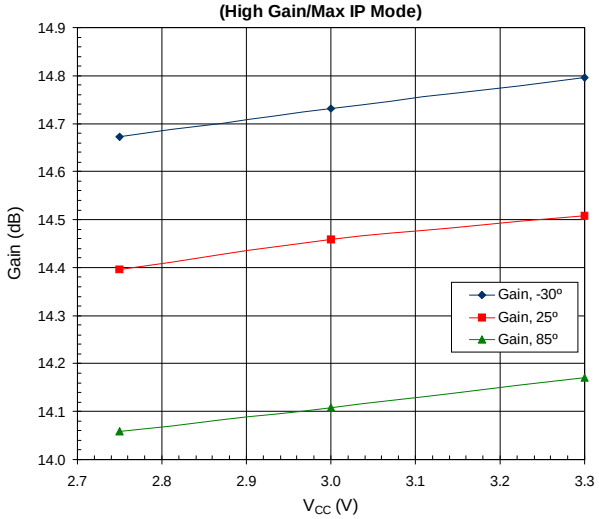
LNA Cellular Band
(High Gain/Nominal IP Mode)



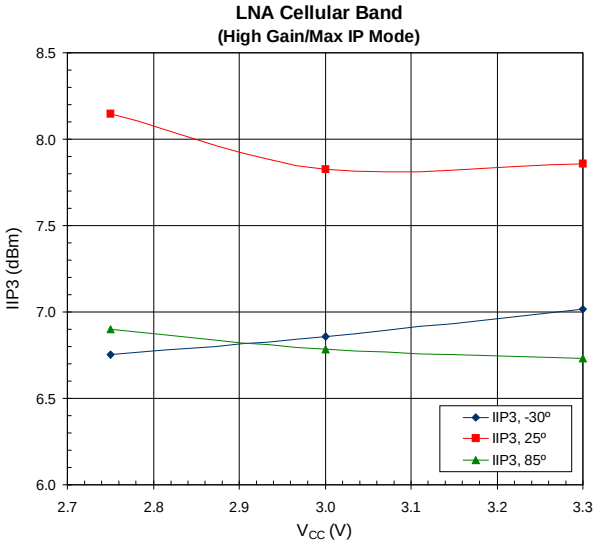
LNA Cellular Band
(High Gain/Nominal IP Mode)

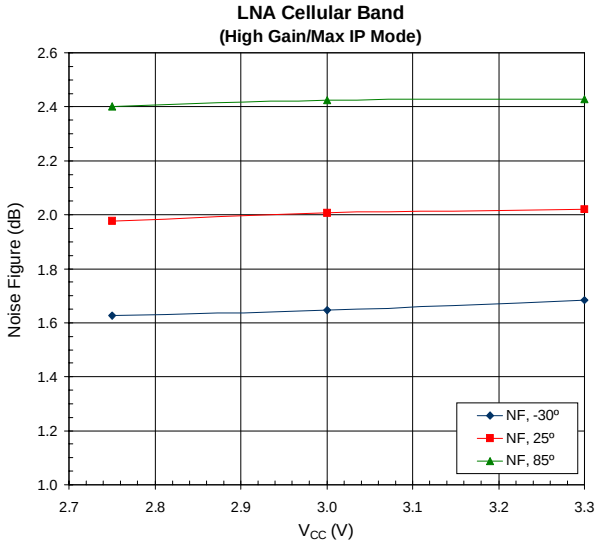


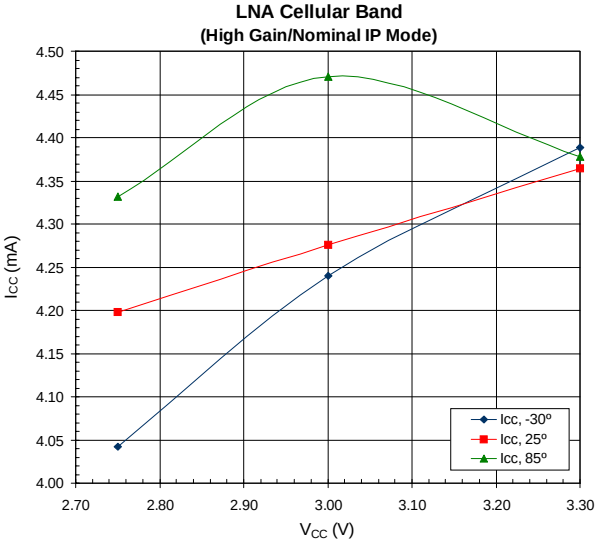
LNA Cellular Band
(High Gain/Max IP Mode)

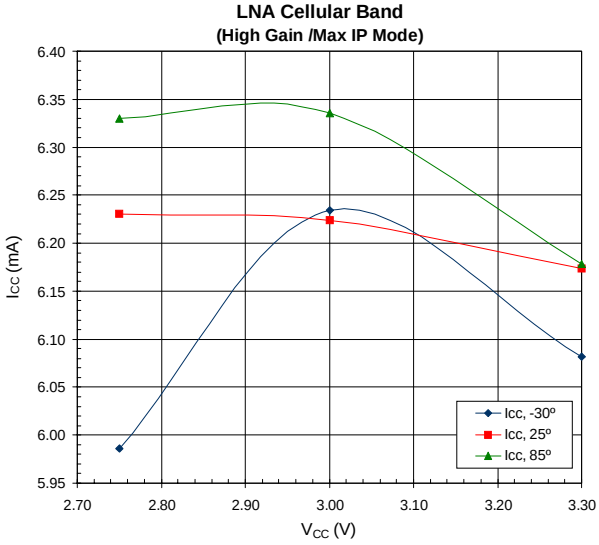


8
FRONT-ENDS

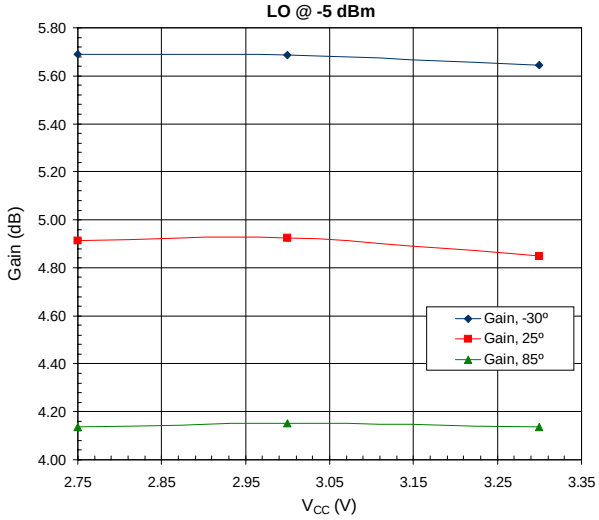

LNA Cellular Band
(High Gain/Max IP Mode)
IIP3 (dBm)
8.5
8.0
7.5
7.0
6.5
6.0
2.7
2.8
2.9
3.0
3.1
3.2
3.3
VCC (V)
IIP3, -30º
IIP3, 25º
IIP3, 85º


LNA Cellular Band
(High Gain/Max IP Mode)
Noise Figure (dB)
2.6
2.4
2.2
2.0
1.8
1.6
1.4
1.2
1.0
2.7
2.8
2.9
3.0
3.1
3.2
3.3
VCC (V)
NF, -30º
NF, 25º
NF, 85º

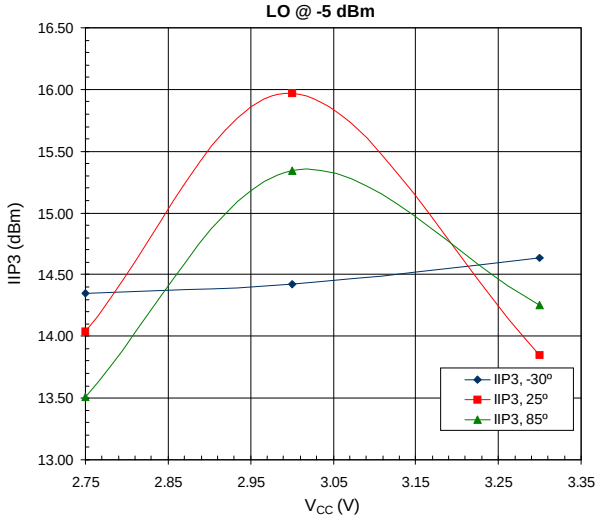

LNA Cellular Band
(High Gain/Nominal IP Mode)
ICC (mA)
4.50
4.45
4.40
4.35
4.30
4.25
4.20
4.15
4.10
4.05
4.00
2.70
2.80
2.90
3.00
3.10
3.20
3.30
VCC (V)
Icc, -30º
Icc, 25º
Icc, 85º


LNA Cellular Band
(High Gain /Max IP Mode)
ICC (mA)
6.40
6.35
6.30
6.25
6.20
6.15
6.10
6.05
6.00
5.95
2.70
2.80
2.90
3.00
3.10
3.20
3.30
VCC (V)
Icc, -30º
Icc, 25º
Icc, 85º

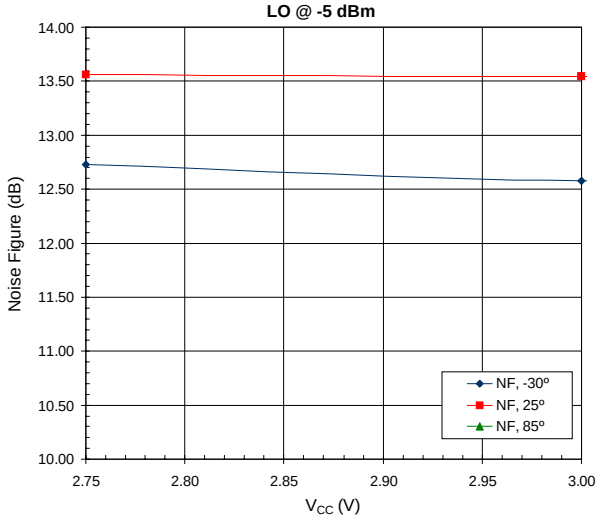
Cellular Band, Mixer IF1, Low Gain Mode, LO @ -5 dBm



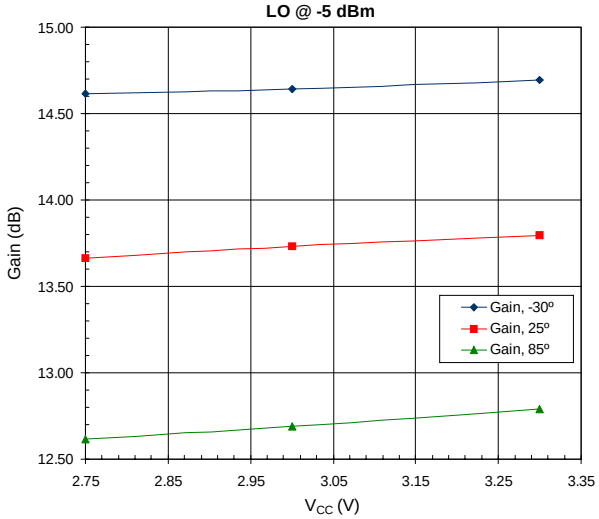
Cellular Band, Mixer IF1, Low Gain Mode, LO @ -5 dBm



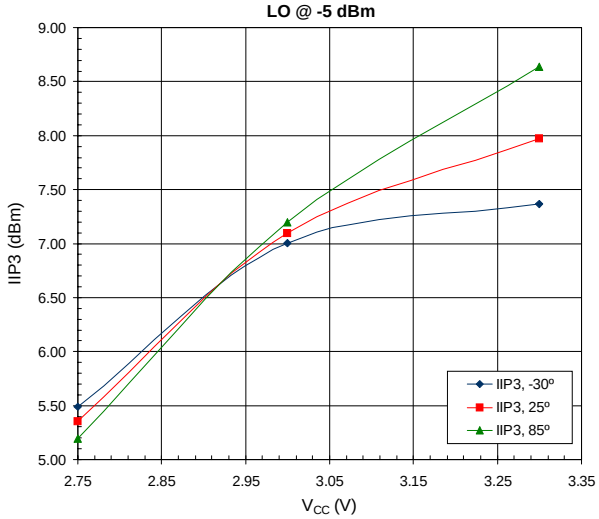
Cellular Band, Mixer IF1, Low Gain Mode, LO @ -5 dBm



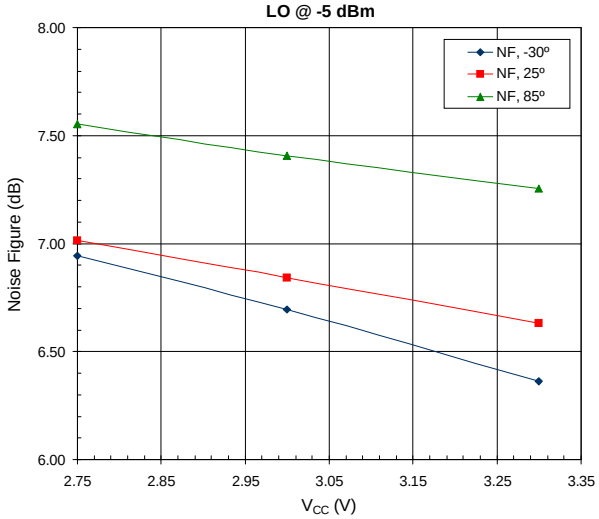
Cellular Band, Mixer IF1, High Gain Mode, LO @ -5 dBm



Cellular Band, Mixer IF1, High Gain Mode, LO @ -5 dBm



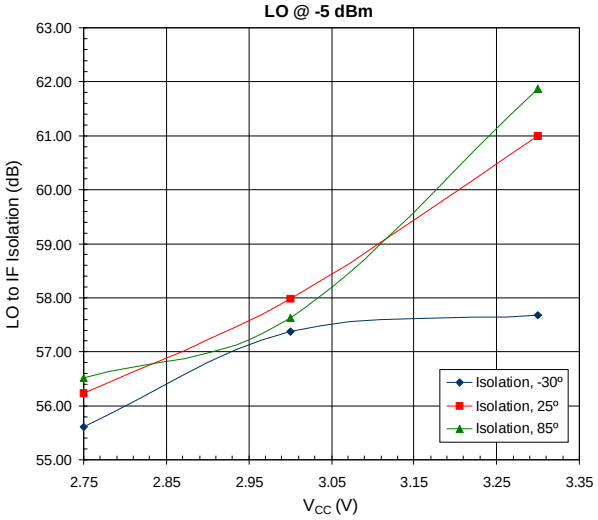
Cellular Band, Mixer IF1, High Gain Mode, LO @ -5 dBm



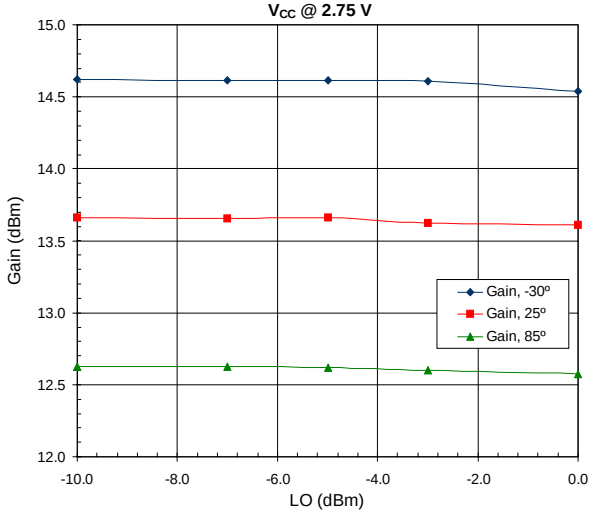
8

FRONT-ENDS

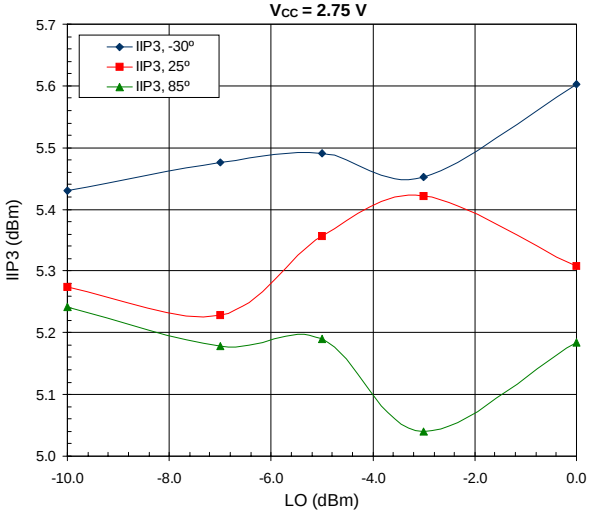
Cellular Band, Mixer IF1, High Gain Mode, LO @ -5 dBm



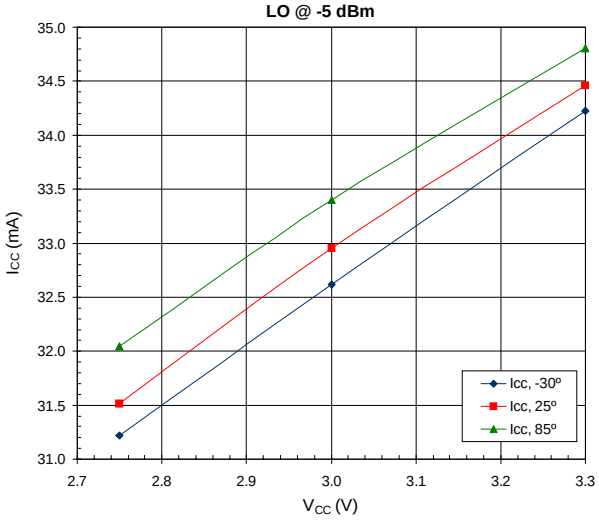
Cellular Band, Mixer IF1, High Gain Mode, V_{CC} @ 2.75 V



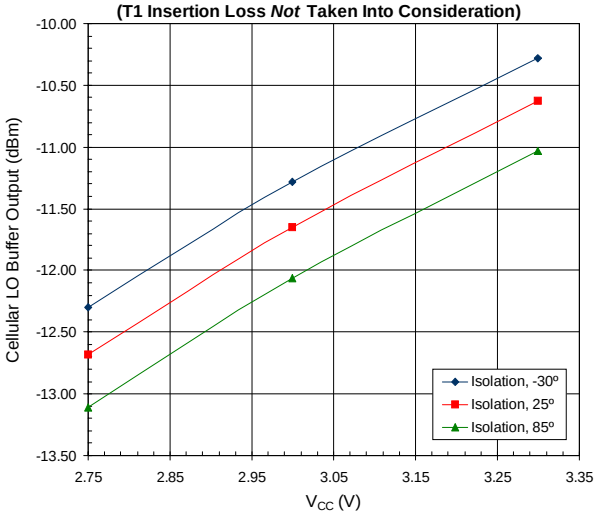
Cellular Band, Mixer IF1, High Gain Mode, V_{CC} = 2.75 V

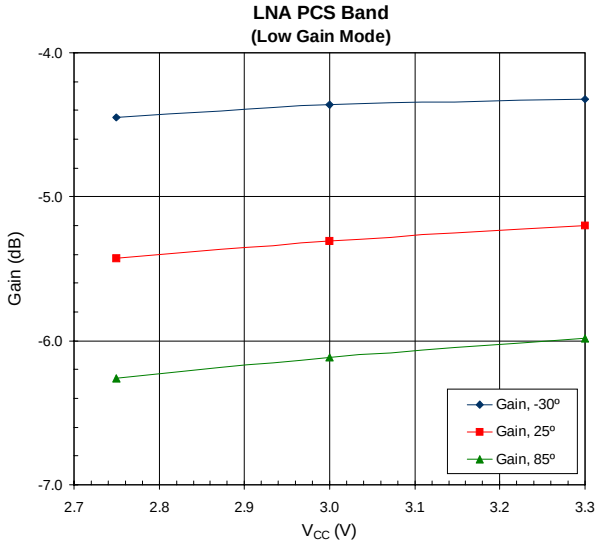


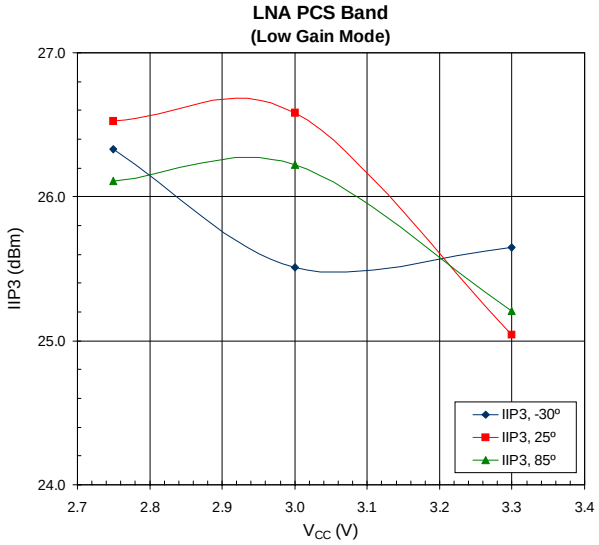
Cellular Band, Mixer IF1, High Gain Mode, LO @ -5 dBm

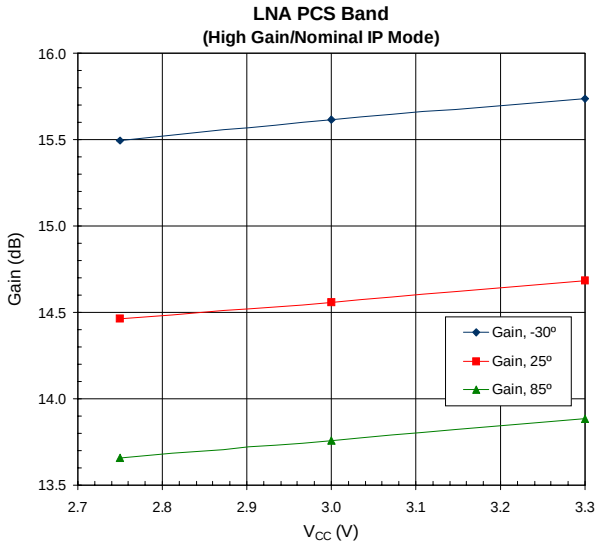


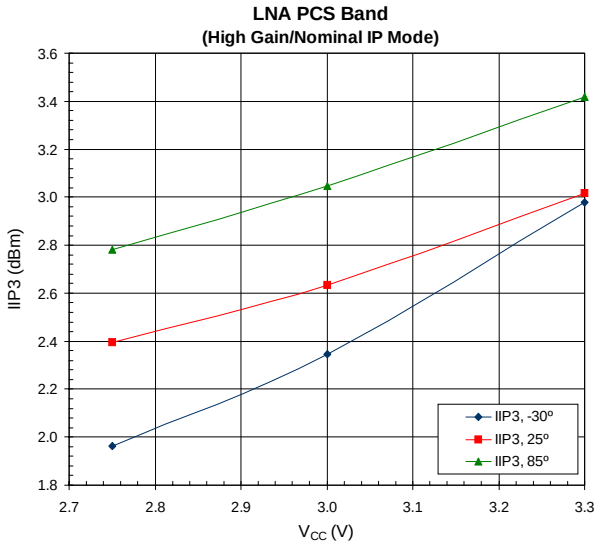
Cellular LO Buffer Output, LO_{IN} @ -5 dBm
(T1 Insertion Loss *Not* Taken Into Consideration)

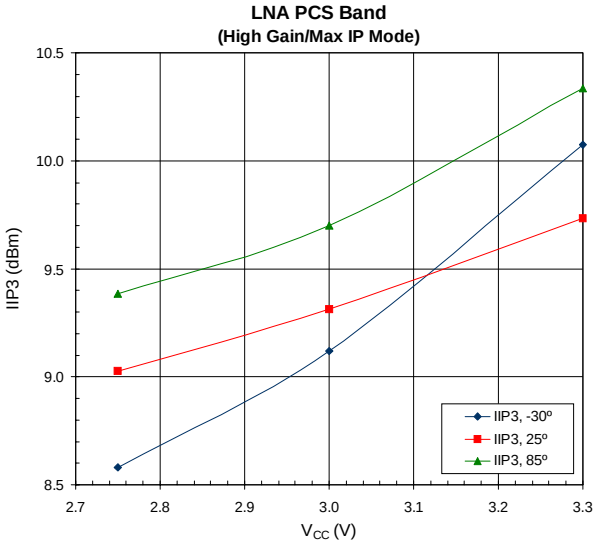


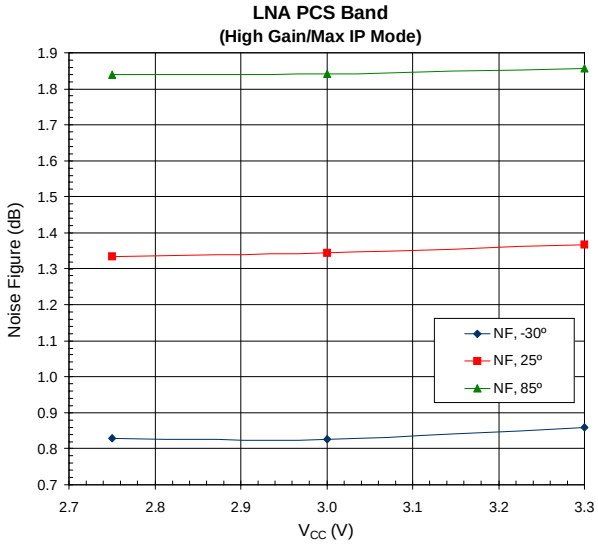

LNA PCS Band
(Low Gain Mode)
Gain (dB)
-4.0
-5.0
-6.0
-7.0
2.7
2.8
2.9
3.0
3.1
3.2
3.3
VCC (V)
Gain, -30º
Gain, 25º
Gain, 85º

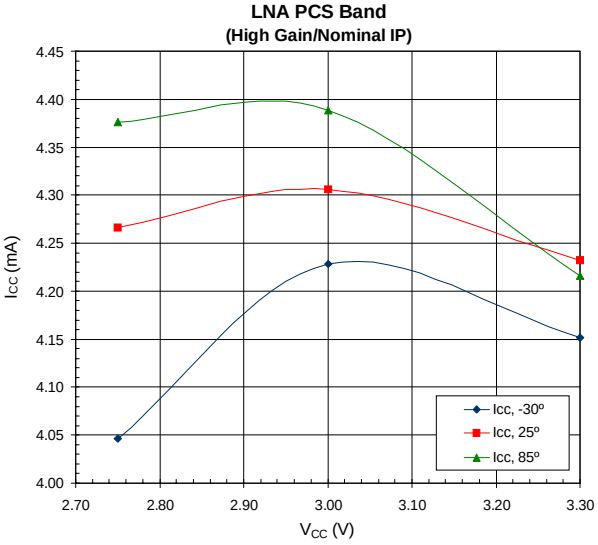

LNA PCS Band
(Low Gain Mode)
IIP3 (dBm)
27.0
26.0
25.0
24.0
2.7
2.8
2.9
3.0
3.1
3.2
3.3
3.4
VCC (V)
IIP3, -30º
IIP3, 25º
IIP3, 85º

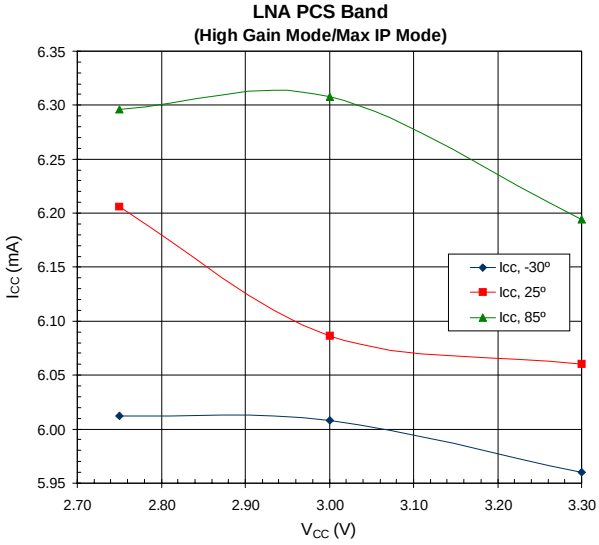

LNA PCS Band
(High Gain/Nominal IP Mode)
Gain (dB)
16.0
15.5
15.0
14.5
14.0
13.5
2.7
2.8
2.9
3.0
3.1
3.2
3.3
VCC (V)
Gain, -30º
Gain, 25º
Gain, 85º

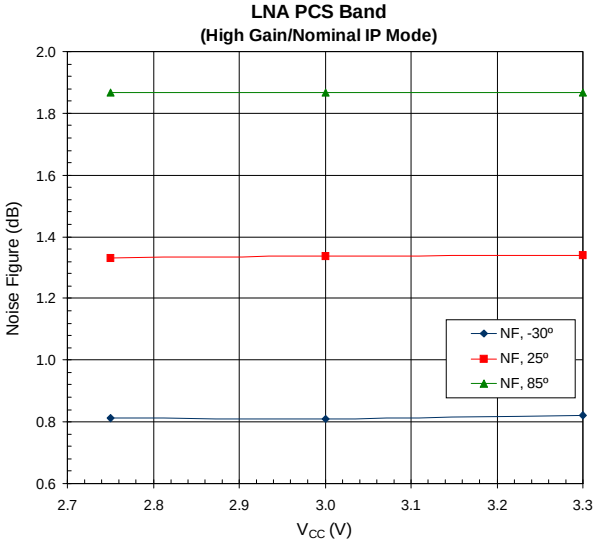

LNA PCS Band
(High Gain/Nominal IP Mode)
IIP3 (dBm)
3.6
3.4
3.2
3.0
2.8
2.6
2.4
2.2
2.0
1.8
2.7
2.8
2.9
3.0
3.1
3.2
3.3
VCC (V)
IIP3, -30º
IIP3, 25º
IIP3, 85º

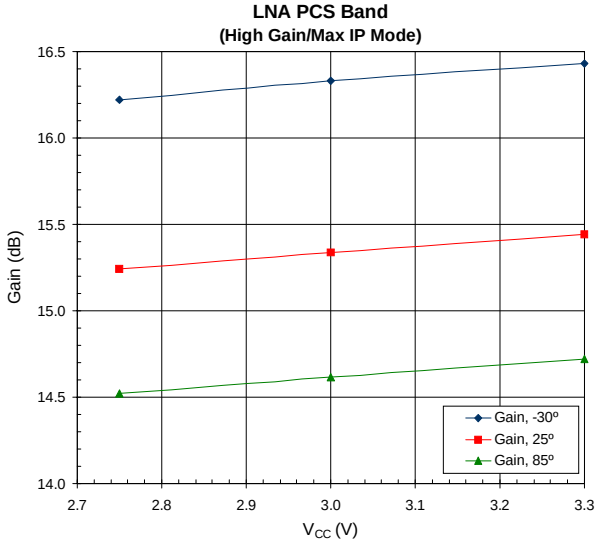

LNA PCS Band
(High Gain/Max IP Mode)
IIP3 (dBm)
VCC (V)
IIP3, -30º
IIP3, 25º
IIP3, 85º

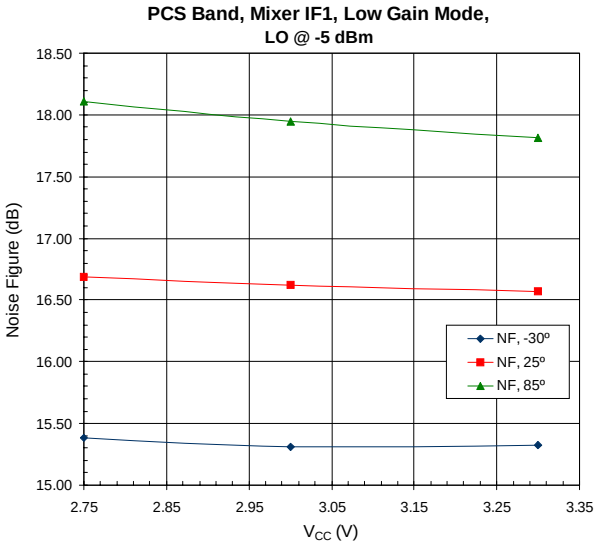

LNA PCS Band
(High Gain/Max IP Mode)
Noise Figure (dB)
VCC (V)
NF, -30º
NF, 25º
NF, 85º

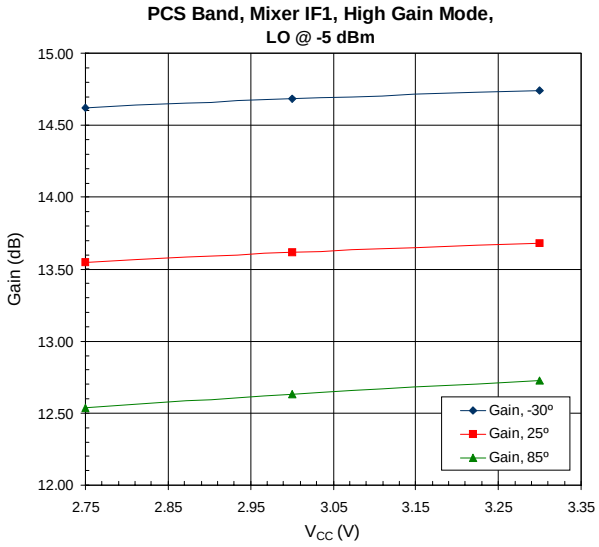

LNA PCS Band
(High Gain/Nominal IP)
ICC (mA)
VCC (V)
Icc, -30º
Icc, 25º
Icc, 85º

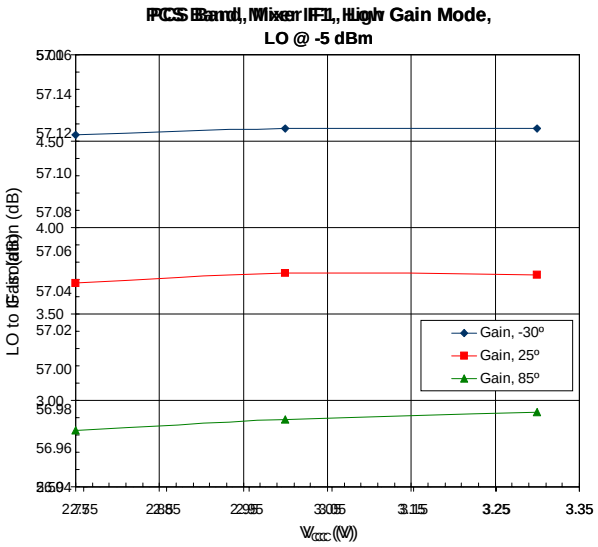

LNA PCS Band
(High Gain Mode/Max IP Mode)
ICC (mA)
VCC (V)
Icc, -30º
Icc, 25º
Icc, 85º

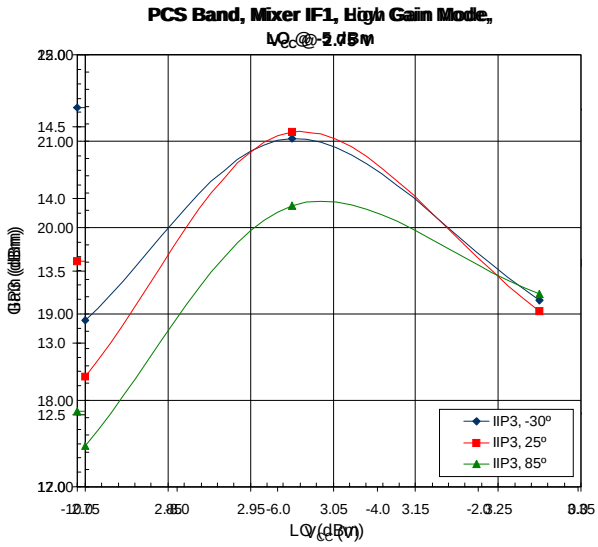

LNA PCS Band
(High Gain/Nominal IP Mode)
Noise Figure (dB)
VCC (V)
NF, -30º
NF, 25º
NF, 85º

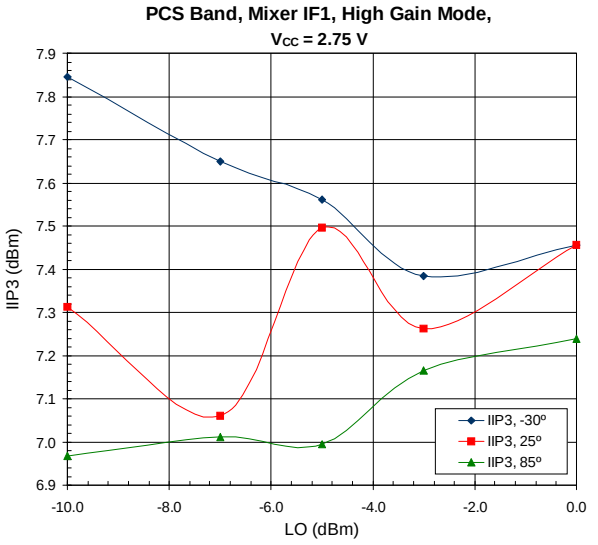

LNA PCS Band
(High Gain/Max IP Mode)
Gain (dB)
VCC (V)
Gain, -30º
Gain, 25º
Gain, 85º

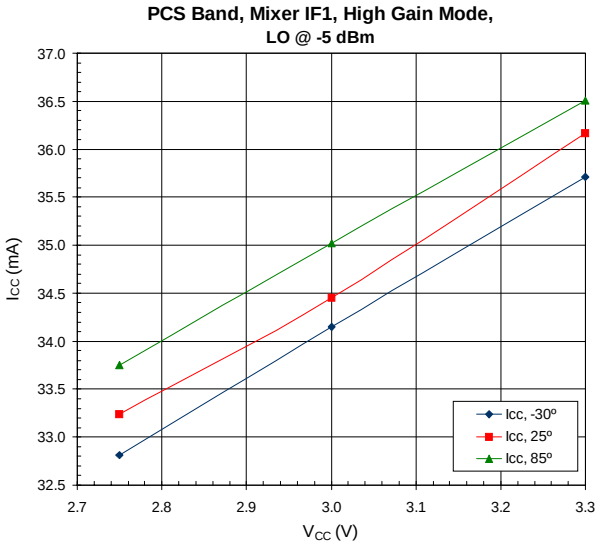

PCS Band, Mixer IF1, Low Gain Mode,
LO @ -5 dBm
Noise Figure (dB)
18.50
18.00
17.50
17.00
16.50
16.00
15.50
15.00
2.75
2.85
2.95
3.05
3.15
3.25
3.35
VCC (V)
NF, -30º
NF, 25º
NF, 85º

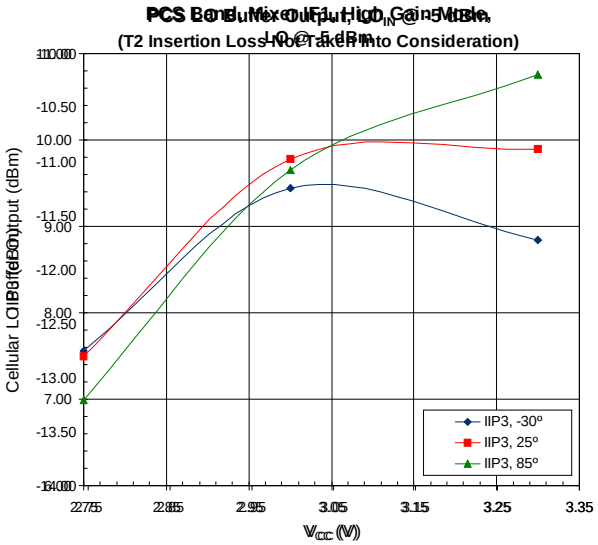

PCS Band, Mixer IF1, High Gain Mode,
LO @ -5 dBm
Gain (dB)
15.00
14.50
14.00
13.50
13.00
12.50
12.00
2.75
2.85
2.95
3.05
3.15
3.25
3.35
VCC (V)
Gain, -30º
Gain, 25º
Gain, 85º

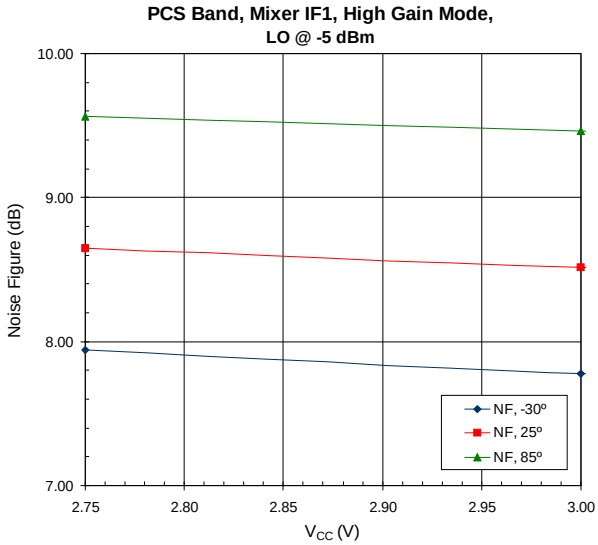

PCS Band, Mixer IF1, High Gain Mode,
LO @ -5 dBm
LO to IF Isolation (dB)
Gain, -30°
Gain, 25°
Gain, 85°
VCC (V)


PCS Band, Mixer IF1, High Gain Mode,
LO @ -5 dBm
IIP3, -30°
IIP3, 25°
IIP3, 85°
LO (dBm)


PCS Band, Mixer IF1, High Gain Mode,
VCC = 2.75 V
IIP3 (dBm)
IIP3, -30°
IIP3, 25°
IIP3, 85°
LO (dBm)


PCS Band, Mixer IF1, High Gain Mode,
LO @ -5 dBm
ICC (mA)
Icc, -30°
Icc, 25°
Icc, 85°
VCC (V)


PCS Band, Mixer IF1, High Gain Mode,
(T2 Insertion Loss Not Taken Into Consideration)
LO @ -5 dBm
Cellular LO Buffer Output (dBm)
IIP3, -30°
IIP3, 25°
IIP3, 85°
VCC (V)


PCS Band, Mixer IF1, High Gain Mode,
LO @ -5 dBm
Noise Figure (dB)
NF, -30°
NF, 25°
NF, 85°
VCC (V)